

ABSTRACT

Signal Sequencing Control Means

A signal sequencing control means is provided for an electronic device. The sequencing control means includes an electronic circuit (2) and timing means, to allow a sequence of control signals to be activated in a pre-determined order for operation of the device and deactivated in a reverse order for disabling the device. The electronic circuit is driven to generate the sequence of control signals in a forward and reverse direction along the same circuit path. The electronic circuit can be a smart card.